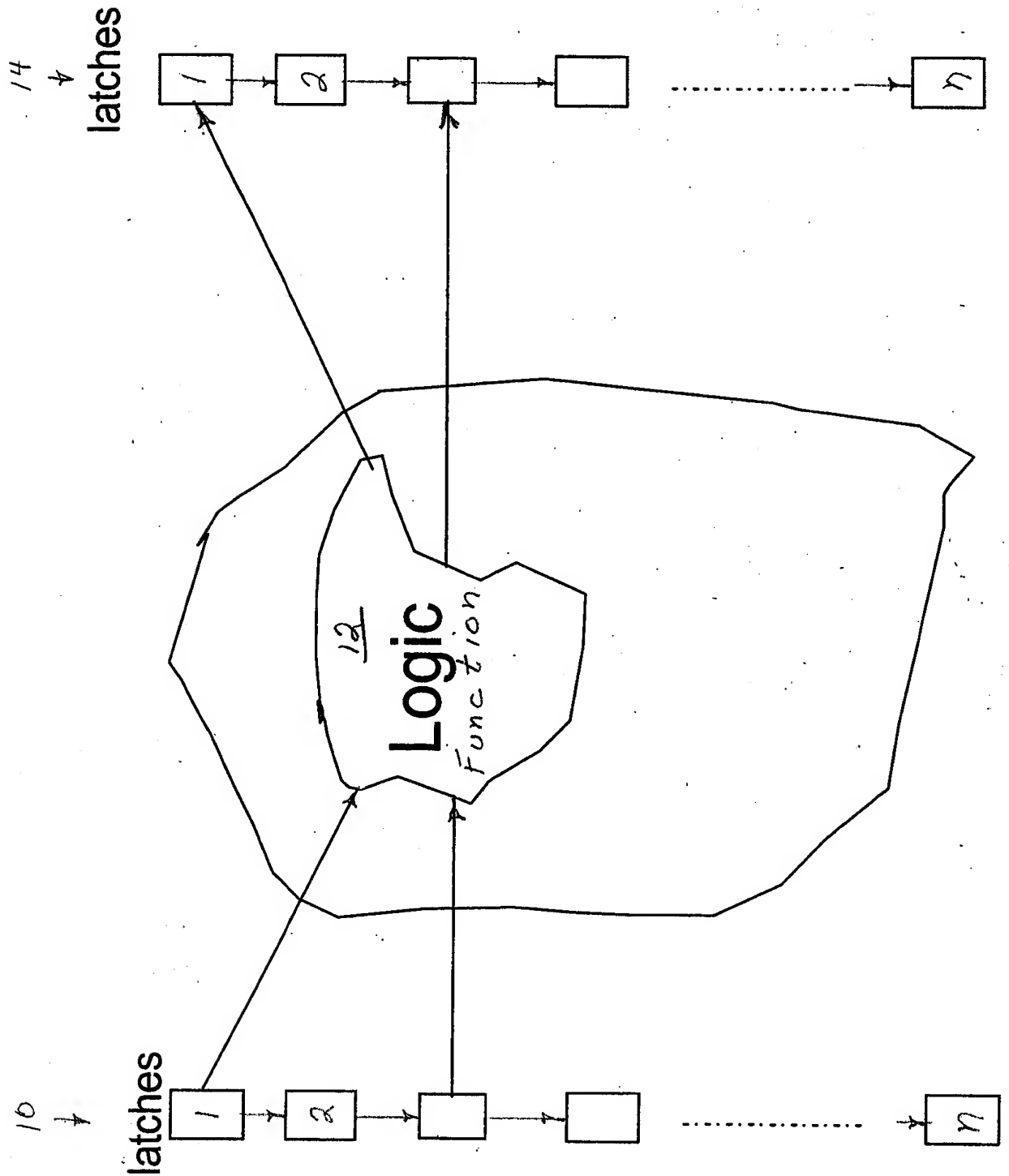
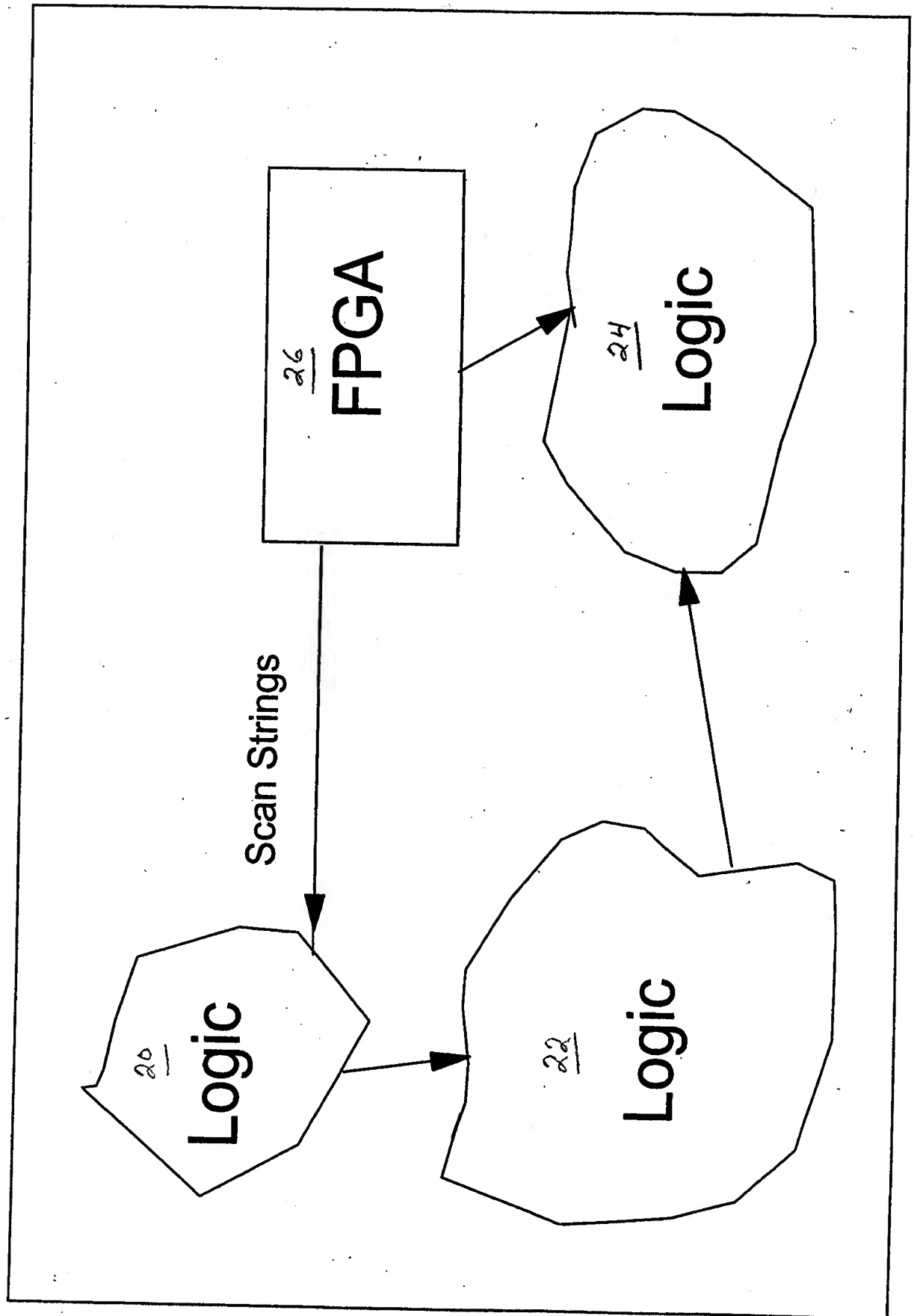


1/3

*Fig. 1*  
**Logic Cones**



*Fig. 2*  
**Structure**



# Function to FPGA

Fig. 3

